

MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, ADVANCED CMOS,
DECODER/DEMULTIPLEXER, MONOLITHIC SILICON, POSITIVE LOGIC

Inactive for new design after 9 August 1996

This amendment forms a part of MIL-M-38510/758A, dated 27 May 1994, and is approved for use by all Departments and Agencies of the Department of Defense.

PAGE 1

1.2.3, delete and substitute:

"1.2.3 Case outlines. The case outlines are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
E	GDIP1-T16 or CDIP2-T16	16	Dual-in-line
F	GDFP2-F16 or CDFP3-F16	16	Flat pack
Z	GDFP1-G16	16	Flat pack with gull wing
2	CQCC1-N20	20	Square leadless chip carrier"

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FIGURE 1; add case outline "Z" under device type "02" next to case outlines "E, F".
FIGURE 1; add case outline "Z" under device type "03" next to case outlines "E, F".

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FSC 5962

AMSC N/A

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MIL-M-38510/758A
AMENDMENT 1

CONCLUDING MATERIAL

Custodians:

Army - CR
Navy - EC
Air Force - 11
NASA - NA
DLA - CC

Preparing activity:
DLA - CC

Review activities:

Army - AR, MI, SM
Navy - AS, CG, MC, OS, SH
Air Force - 19, 99

(Project 5962-1928)